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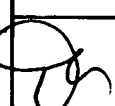

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Sheet 1 of 1

Complete if Known

Application Number	10/654,318
Filing Date	September 3, 2003
First Named Inventor	Javendar Rajagopalan
Art Unit	2817
Examiner Name	(Not Yet Assigned)
Attorney Docket Number	P05655

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	AA	MARGARIT, Mihai A., et al, "A Low-Noise, Low-Power VCO With Automatic Amplitude Control for Wireless Applications," IEEE Journal of Solid-State Circuits, Vol. 34, No. 6, June 1999, pp. 761-771.	
	AB	ROGERS, John W.M., et al., "A Study of Digital and Analog Automatic-Amplitude Control Circuitry for Voltage-Controlled Oscillators," IEEE Journal of Solid-State Circuits, Vol. 38, No. 2, February 2003, pp. 352-356.	

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